Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	183	switch near4 fabric same stage	USPAT	OR	OFF	2005/07/11 13:05
S2	161	switch near4 fabric same stage and port	USPAT	OR	OFF	2005/07/11 13:06
<b>S3</b>	48	switch near4 fabric same stage and port and crossbar	USPAT	OR	OFF	2005/07/11 13:07
S4	0	switch near4 fabric same stage and port and crossbar and bid	USPAT	OR	OFF	2005/07/11 13:07
S5	36	switch near4 fabric same stage and port and crossbar and (time near3 slot or channel)	USPAT	OR	OFF	2005/07/12 09:45
S6	60	switch same (three-stage three near2 stage) and port and crossbar	USPAT	OR	OFF	2005/07/12 11:16
S7	5	340/2.22.ICLS.	USPAT	OR	OFF	2005/07/12 11:08
S8	61	340/2.22.CCLS.	USPAT	OR	OFF	2005/07/12 11:09
S9	60	switch same (three-stage three near2 stage) and port and crossbar	USPAT	OR	OFF	2005/07/12 11:09
S10	6	S8 and S9	USPAT	OR	OFF	2005/07/12 11:09
S11	22	switch same (three-stage three near2 stage) and port and crossbar and expansion	USPAT	OR	OFF	2005/07/12 11:18
S12	19	switch same (three-stage three near2 stage) and port and crossbar and expansion and stor\$3	USPAT	OR	OFF	2005/07/12 11:18
S13	8	switch same (three-stage three near2 stage) and port and crossbar and expansion and stor\$3 near5 cell	USPAT	OR	OFF	2005/07/13 15:08
S14	61	340/2.22.CCLS.	USPAT	OR	OFF	2005/07/12 15:16
S15	43	340/2.24.CCLS.	USPAT	OR	OFF	2005/07/12 15:40
S16	65	340/2.26.CCLS.	USPAT	OR	OFF	2005/07/12 15:47
S17	148	370/380.CCLS.	USPAT	OR	OFF	2005/07/13 16:28
S18	74	370/427.CCLS.	USPAT	OR '	OFF	2005/07/13 15:07
S19	14	switch adj fabric same (three-stage three near2 stage) and crossbar and stor\$3	USPAT	OR	OFF	2005/07/13 15:29
S20	6	switch adj fabric same (three-stage three near2 stage) and crossbar same stor\$3 and (arbitrator control\$3) and memory	USPAT	OR	OFF	2005/07/13 15:38
S21	6	switch adj fabric same (three-stage three near2 stage) and crossbar same stor\$3 and (arbitrator control\$3 determ\$5) and memory	USPAT	OR	OFF	2005/07/13 15:39

S22		switch adj fabric same (three-stage three near2 stage) and crossbar same stor\$3 and (arbitrator control\$3 determ\$5) and (memory sav\$3) and output and cell	USPAT	OR	ON	2005/07/13 15:46
S23	7	switch adj fabric same (three-stage three near2 stage) and crossbar same stor\$3 and (arbitrator control\$3 determ\$5) and (memory sav\$3 stor\$3) same output same cell	USPAT	OR	ON	2005/07/13 15:47
S24	5	switch adj fabric same (three-stage three near2 stage) and crossbar same (arbitrator control\$3 determ\$5) and (memory sav\$3 stor\$3) same output same cell	USPAT	OR	ON	2005/07/13 15:54
S25	59	switch adj fabric and crossbar same (arbitrator control\$3 determ\$5) and (memory sav\$3 stor\$3) same output same cell	USPAT	OR	ON	2005/07/13 16:14
S26	8	switch adj fabric and crossbar same (arbitrator control\$3 determ\$5) and (memory sav\$3 stor\$3) same output same cell same time adj slot	USPAT	OR	ON	2005/07/13 16:12
S27	269	370/388.CCLS.	USPAT	OR	OFF	2005/07/13 16:13
S28	5	S27 and switch adj fabric and crossbar same (arbitrator control\$3 determ\$5) and (memory sav\$3 stor\$3) same output same cell	USPAT .	OR	ON	2005/07/13 16:17
S29	30	S27 and switch adj fabric and (arbitrator control\$3 determ\$5) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 16:29
S30	74	370/427.CCLS.	USPAT	OŖ	OFF	2005/07/13 17:12
S31	20	S30 and switch adj fabric and (arbitrator control\$3 determ\$5) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 17:13
S32	1010	370/466.CCLS.	USPAT	OR	OFF	2005/07/13 21:37
S33	51	S32 and switch adj fabric and (arbitrator control\$3 determ\$5) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 23:52
S34	. 0	S32 and switch adj fabric and (arbitrator control\$3 determ\$5) same (same\$2time same-time) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 17:22
S35	0	S32 and switch adj fabric and (arbitrator control\$3 determ\$5) and (same\$2time same-time) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 17:23

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S36	0	switch adj fabric and (arbitrator control\$3 determ\$5) and (same\$2time same-time (same\$2cell adj slot)) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 17:25
S37	207477	switch adj fabric and (arbitrator control\$3 determ\$5) and (same\$2time same-time (same\$2cell)) slot and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 17:25
S38	0	switch adj fabric and (arbitrator control\$3 determ\$5) and (same\$2time same-time (same\$2cell)) adj slot and (memory sav\$3 stor\$3)	USPAT	OR	ON ·	2005/07/13 17:30
S39	163	switch adj fabric and (arbitrator control\$3 determ\$5) and ((same\$2time same-time (same\$2cell)) adj slot or collision) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 17:51
S40	5	switch adj fabric and (arbitrator control\$3 determ\$5) and collision same (time cell) adj slot and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 17:57
S41	77	switch adj fabric and (arbitrator control\$3 determ\$5) and collision and ((time cell) adj slot or time\$1slot cell\$1slot) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 21:38
S42	322	370/467.CCLS.	USPAT	OR	OFF	2005/07/13 21:50
S43	12	S42 and switch adj fabric and (arbitrator control\$3 determ\$5) and ((time cell) adj slot or time\$1slot cell\$1slot) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/13 21:38
S44	15	370/367.CCLS.	USPAT	OR	OFF	2005/07/13 23:51
S45	137	370/366.CCLS.	USPAT	OR	OFF	2005/07/13 23:51
S46		S45 and switch adj fabric and (arbitrator control\$3 determ\$5) and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 00:09
S47	857	switch and (arbitrator control\$3 determ\$5) same (more plural) adj cell and (memory sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 00:12
S48	. 37	switch and (arbitrator control\$3 determ\$5) and (accept or reject) same (more plural) adj cell and (memory buffer sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 00:16

S49	10	switch and (arbitrator control\$3 determ\$5) same (accept or reject) same (more plural) adj cell and (memory buffer sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 00:16
S50	. 7	switch and (arbitrator control\$3 determ\$5) same (accept or reject) same (more plural) adj cell and (memory buffer sav\$3 stor\$3) same output	USPAT	OR	ON .	2005/07/14 00:21
S51	0	switch and (arbitrator control\$3 determ\$5) same (accept or reject) same (request bid) same (more plural) adj cell and (memory buffer sav\$3 stor\$3) same output	USPAT	OR	ON	2005/07/14 00:22
S52	. 26	switch and (arbitrator control\$3 determ\$5) same (accept or reject) and (request bid ) same (more plural) adj cell and (memory buffer sav\$3 stor\$3) same output	USPAT	OR	ON	2005/07/14 00:43
S53	272	switch and (arbitrator controler determinator) and (accept or reject) and (request bid ) same (more plural) and (memory buffer sav\$3 stor\$3) same output	USPAT	OR	ON	2005/07/14 00:45
S54	57	switch and (arbitrator controler determinator) same(accept or reject) and (request bid ) same (more plural) and (memory buffer sav\$3 stor\$3) same output	USPAT	OR	ON	2005/07/14 00:45
S55	57	switch and (arbitrator controler determinator) same (accept or reject) and (request bid ) same (more plural) and (memory buffer sav\$3 stor\$3) same output	USPAT	OR	ON	2005/07/14 10:54
S56	17	switch near3 fabric and (arbitrator controler determinator) and (accept or reject) and (request bid ) same (more plural) and (memory buffer sav\$3 stor\$3) same output	USPAT	OR	ON	2005/07/14 11:48
S57	0	"5612953".PN. and switch and (arbitrator controler determinator) and (accept or reject) and (request bid ) same (more plural) and (memory buffer sav\$3 stor\$3) same output	USPAT	OR	ON	2005/07/14 11:49
S58	0	"5612953".PN. and switch and (arbitrator controler determinator) and (accept or reject) and (request bid) same (more plural) and (memory buffer sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 11:49

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S59		"5612953".PN. and switch and (accept or reject) and (request bid ) same (more plural) and (memory buffer sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 13:40
S60	954	switch and (accept or reject or request bid ) and port near5 expansion and (more plural) and (memory buffer sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 13:44
S61	906	switch and (accept or reject or request bid ) and port near5 expansion and (controler arbitrator determ\$5) and (more plural) and (memory buffer sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 13:45
S62	46	switch near5 fabric and (accept or reject or request bid ) and port near5 expansion and (controler arbitrator determ\$5) and (more plural) and (memory buffer sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 14:30
S63	. · ·	switch and (three\$1stage) and (accept or reject or request bid ) and port near5 expansion and (controler arbitrator determ\$5) and (more plural) and (memory buffer sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 14:38
S64	6	switch and (accept or reject or request bid ) and port near5 expansion and (controler arbitrator) same determine and (more plural) same (memory buffer sav\$3 stor\$3)	USPAT	OR	ON	2005/07/14 14:42
S65	1	"6771596".PN.	USPAT	OR	OFF	2005/07/14 14:49
S66	1	"6445705".PN.	USPAT	OR	OFF	2005/07/14 14:50
S67	1	"6882766".PN.	USPAT	OR	OFF	2005/07/14 15:04
S68	1005	tdma with time adj slot and "370". clas.	USPAT	OR	OFF	2005/07/14 15:05
S69	16	tdma with time adj slot and "370". clas. and @py<"1980"	USPAT	OR	OFF	2005/07/14 15:06
S70	12	tdma with time adj slot and "370". clas. and @py<"1980" and multiplex\$3	USPAT	OR	OFF	2005/07/14 15:17
S71	1	"6519225".PN.	USPAT	OR	OFF	2005/07/14 15:23
S72	0	"6519225".PN. and time adj slot and port and manager and expansion	USPAT	OR	ON	2005/07/14 15:25
S73	1	"6519225".PN. and time adj slot and port and manager and memory	USPAT	OR	ON	2005/07/14 15:37
S74	1	"6519225".PN. and time adj slot and port and manager and memory and grant	USPAT	OR	ON	2005/07/14 15:38

S75	1	"6519225".PN. and time adj slot and port and manager and memory and grant same crossbar	USPAT	OR	ON	2005/07/14 15:43
S76	1	"6519225".PN. and time adj slot and port and manager and memory and grant and crossbar	USPAT	OR	ON .	2005/07/14 16:16
S77	1	"6314102".PN.	USPAT	OR	ON	2005/07/14 16:16
S78	44	("5181106"   "5189673"   "5191456"   "5283678"   "5301050"   "5303229"   "5446726"   "5495483"   "5512936"   "5519731"   "5521906"   "5533008"   "5534912"   "5539777"   "5557612"   "5572347"   "5576874"   "5581479"   "5594491"   "5594576"   "5596604"   "5604682"   "5606577"   "5608725"   "5612742"   "5613190"   "5619505"   "5623513"   "5623543"   "5625651"   "5636139"   "5636224"   "5668857"   "5687014"   "5756280"   "5790548"   "5812786"   "5818511"   "5852655"   "5905781"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/14 19:07
S79	0	"60255749".PN.	USPAT	OR	OFF	2005/07/14 19:09
S80	. 12	("5267235"   "5493566"   "5500858"   "5577035"   "5742606"   "5835491"   "5978359"   "6141346"   "6201789"   "6201792"   "6259698"   "6324165").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/14 20:15
S82	30	switch near6 fabric and (manager arbitrator controler) and (port card) near9 (expan\$4 extend\$4) and (memory sav\$3 stor\$3) and stage	USPAT	OR	OFF	2005/07/15 11:16
S83	56	switch near6 fabric and (manager arbitrator controler) and (port card) near9 (expan\$4 extend\$4) and (memory sav\$3 stor\$3)	USPAT	OR	OFF	2005/07/15 15:07
S84	1	"6778538".PN.	USPAT	OR	OFF	2005/07/15 14:17
S85	1	"6778536".PN.	USPAT	OR	OFF	2005/07/15 15:06
S86	1	"6778536".PN. and stage	USPAT	OR	OFF	2005/07/15 15:07
S87	0	S86 and switch near6 fabric and (manager arbitrator controler) and (port card) near9 (expan\$4 extend\$4) and (memory sav\$3 stor\$3)	USPAT	OR	OFF	2005/07/15 15:07
S88	0	S86 and switch near6 fabric and (manager arbitrator controler) and (port card) and (memory sav\$3 stor\$3)	USPAT	OR	OFF	2005/07/15 15:08

S89	1	S86 and switch near6 fabric and (port card) and (memory sav\$3 stor\$3)	USPAT	OR	OFF	2005/07/15 15:12
S90	1	S86 and switch near6 fabric and (manager arbitrator controller) and (port card) and (memory sav\$3 stor\$3)	USPAT	OR	OFF	2005/07/15 15:38
S91	0	S86 and switch near6 fabric and (manager arbitrator controller) and (port card) and (memory sav\$3 stor\$3) and contension same time adj slot	USPAT	OR	OFF	2005/07/15 15:38
S92	0	S86 and switch near6 fabric and (manager arbitrator controller) and (port card) and (memory sav\$3 stor\$3) and contendsion same time adj slot	USPAT	OR	OFF	2005/07/15 15:39
S93		switch near6 fabric and (manager arbitrator controller) and (port card) and (memory sav\$3 stor\$3) and contendsion same time adj slot	USPAT	OR	OFF	2005/07/15 15:39
S94	24	switch near6 fabric and (manager arbitrator controller) and (port card) and (memory sav\$3 stor\$3) and contention same time adj slot	USPAT	OR	OFF	2005/07/15 22:36
S95	36	switch near6 fabric and (manager arbitrator controller) and (port card) and (memory sav\$3 stor\$3) and contention and (time\$1slot)	USPAT	OR	OFF	2005/07/15 22:35
S96	1	"6778536".PN.	USPAT	OR	OFF	2005/07/15 21:02
S97	0	S96 and switch near6 fabric and (manager arbitrator controller) and (port card) and (memory sav\$3 stor\$3) and contention and (time\$1slot)	USPAT	OR .	OFF	2005/07/15 21:03
S98	0	S96 and switch and (port card) and (memory sav\$3 stor\$3) and contention and (time\$1slot)	USPAT	OR	OFF	2005/07/15 21:04
S99	0	S96 and switch and (port card) and (memory sav\$3 stor\$3) and (time\$1slot)	USPAT	OR	OFF	2005/07/15 21:04
S10 0	<b>1</b>	S96 and switch and (port card)	USPAT	OR	OFF	2005/07/15 21:04
S10 1	1	"5255265".PN.	USPAT	OR	OFF	2005/07/16 02:20
S10 2	0	"5157654".PN. and card	USPAT	OR	OFF	2005/07/16 02:21
S10 3	0	"5157654".PN. and egress near2 port	USPAT	OR	ON	2005/07/16 02:22

S10							
S   S   S   S   S   S   S   S   S   S		1	"5157654".PN.	USPAT	OR	ON	2005/07/16 02:22
6         S10         1         "5157654".PN. and "230"         USPAT         OR         ON         2005/07/16 03:30           7         S10         2315         switch and (three\$1stage)         USPAT         OR         ON         2005/07/16 03:31           S10         1131         switch and (three\$1stage) and cross         USPAT         OR         ON         2005/07/16 03:31           S11         32         switch and (three\$1stage) and cross and contention         USPAT         OR         ON         2005/07/16 03:32           S11         27         switch and (three\$1stage) and cross and contention and (arbitrator controller manager)         USPAT         OR         ON         2005/07/16 03:33           S11         3         switch and (three\$1stage) and cross and contention and (arbitrator controller manager) and time\$1slot         USPAT         OR         ON         2005/07/16 03:33           S11         163         (atm cross\$1bar cross\$1point) and switch and (contention collision) and (arbitrator controller manager) and time\$1slot         USPAT         OR         ON         2005/07/16 20:02           S11         148         S113 and memory and (grant reject\$3 accept\$3 bid service\$2*request)         USPAT         OR         OFF         2005/07/16 20:07           S11         36         (atm cross\$1bar cross\$1point) and	1	0	"5157654".PN. and card	USPAT	OR	ON	2005/07/16 02:24
7   131	1	0	"5157654".PN. and mux	USPAT	OR	ON	2005/07/16 02:24
8	1	1	"5157654".PN. and "230"	USPAT	OR	ON	2005/07/16 03:30
9	1	2315	switch and (three\$1stage)	USPAT	OR	ON	2005/07/16 03:31
0         cross and contention         USPAT         OR         ON         2005/07/16 03:33           511         27         switch and (three\$1stage) and cross and contention and (arbitrator controller manager)         USPAT         OR         ON         2005/07/16 03:33           511         3         switch and (three\$1stage) and cross and contention and (arbitrator controller manager) and time\$1slot         USPAT         OR         ON         2005/07/16 20:02           511         163         (atm cross\$1bar cross\$1point) and switch and ( contention collision) and (arbitrator controller manager) and time\$1slot         USPAT         OR         ON         2005/07/16 20:17           511         148         \$113 and memory and (grant reject\$3 accept\$3 bid service\$2request)         USPAT         OR         OFF         2005/07/16 20:05           511         36         (atm cross\$1bar cross\$1point) and switch and ( contention collision) and (two or more near5 colli\$4) with time\$1slot         USPAT         OR         ON         2005/07/16 20:37           511         8         (atm cross\$1bar cross\$1point) and switch and ( contention collision) same (two or more near5 colli\$4) with time\$1slot         USPAT         OR         ON         2005/07/16 20:20           511         28         \$116 not \$117         USPAT         OR         ON         2005/07/16 21:14           511 <td>1</td> <td>1131</td> <td>, , ,</td> <td>USPAT</td> <td>OR ·</td> <td>ON</td> <td>2005/07/16 03:31</td>	1	1131	, , ,	USPAT	OR ·	ON	2005/07/16 03:31
1	1	32		USPAT	OR	ON	2005/07/16 03:32
2         cross and contention and (arbitrator controller manager) and time\$1slot           S11         163         (atm cross\$1bar cross\$1point) and switch and ( contention collision) and (arbitrator controller manager) and time\$1slot         USPAT         OR         ON         2005/07/16 20:17           S11         148         S113 and memory         USPAT         OR         OFF         2005/07/16 20:05           S11         124         S113 and memory and (grant reject\$3 bid service\$2 request)         USPAT         OR         OFF         2005/07/16 20:07           S11         36         (atm cross\$1bar cross\$1point) and switch and (contention collision) and (two or more near5 colli\$4) with time\$1slot         USPAT         OR         ON         2005/07/16 20:20           S11         8         (atm cross\$1bar cross\$1point) and switch and (contention collision) same (two or more near5 colli\$4) with time\$1slot         USPAT         OR         ON         2005/07/16 20:20           S11         28         S116 not S117         USPAT         OR         ON         2005/07/16 21:14           S11         1         "5612953".PN.         USPAT         OR         ON         2005/07/16 21:28           S12         0         "5612953".PN. and input near2 (stage device)         USPAT         OR         ON         2005/07/16 21:29           S	1	27	cross and contention and (arbitrator	USPAT	OR	ON	2005/07/16 03:33
Switch and ( contention collision) and (arbitrator controller manager) and time\$1slot	1	3	cross and contention and (arbitrator	USPAT	OR	ON	2005/07/16 20:02
4 S11		163	switch and ( contention collision) and (arbitrator controller manager)	USPAT	OR	ON	2005/07/16 20:17
Teject\$3 accept\$3 bid   Service\$2request)   S11   36   (atm cross\$1bar cross\$1point) and   Switch and ( contention collision)   and (two or more near5 colli\$4)   with time\$1slot   USPAT   OR   ON   2005/07/16 20:37	1	148	S113 and memory	USPAT	OR	OFF	2005/07/16 20:05
6       switch and ( contention collision) and (two or more near5 colli\$4) with time\$1slot       USPAT       OR       ON       2005/07/16 20:20         S11       8 (atm cross\$1bar cross\$1point) and switch and ( contention collision) same (two or more near5 colli\$4) with time\$1slot       USPAT       OR       ON       2005/07/16 20:20         S11       28 S116 not S117       USPAT       OR       ON       2005/07/16 21:14         S11       1 "5612953".PN.       USPAT       OR       ON       2005/07/16 21:14         S12       0 "5612953".PN. and time\$1slot       USPAT       OR       ON       2005/07/16 21:28         S12       0 "5157654".PN. and input near2 (stage device)       USPAT       OR       ON       2005/07/16 21:29         S12       1 "5157654".PN. and input       USPAT       OR       ON       2005/07/16 21:29		124	reject\$3 accept\$3 bid	USPAT	OR	OFF	2005/07/16 20:07
7       switch and ( contention collision) same (two or more near5 colli\$4) with time\$1slot       USPAT       OR       ON       2005/07/16 21:14         S11       28       S116 not S117       USPAT       OR       ON       2005/07/16 21:14         S11       1       "5612953".PN.       USPAT       OR       ON       2005/07/16 21:14         S12       0       "5612953".PN. and time\$1slot       USPAT       OR       ON       2005/07/16 21:28         S12       0       "5157654".PN. and input near2 (stage device)       USPAT       OR       ON       2005/07/16 21:29         S12       1       "5157654".PN. and input       USPAT       OR       ON       2005/07/16 21:37		36	switch and ( contention collision) and (two or more near5 colli\$4)	USPAT	OR	ON	2005/07/16 20:37
8 S11		8	switch and ( contention collision) same (two or more near5 colli\$4)	USPAT	OR	ON	2005/07/16 20:20
9 S12 0 "5612953".PN. and time\$1slot USPAT OR ON 2005/07/16 21:28  S12 1 "5157654".PN. and input near2 (stage device)  S12 1 "5157654".PN. and input USPAT OR ON 2005/07/16 21:29  CON CON CON CON CON CON CON CON CON CO	i i	28	S116 not S117	USPAT	OR	ON	2005/07/16 21:14
0   S12   0 "5157654".PN. and input near2   USPAT   OR   ON   2005/07/16 21:29   S12   1 "5157654".PN. and input   USPAT   OR   ON   2005/07/16 21:37		1	"5612953".PN.	USPAT	OR	ON	2005/07/16 21:14
1 (stage device) S12 1 "5157654".PN. and input USPAT OR ON 2005/07/16 21:37	ı	0	"5612953".PN. and time\$1slot	USPAT	OR	ON	2005/07/16 21:28
	1	0		USPAT	OR	ON	2005/07/16 21:29
	1	1	"5157654".PN. and input	USPAT	OR	ON	2005/07/16 21:37

S12 3	1	"5157654".PN. and input adj module	USPAT	OR	ON	2005/07/16 22:04
S12 4	1	"5157654".PN. and output adj module	USPAT	OR	ON	2005/07/16 21:56
S12 5	1	"5157654".PN. and cross\$1point	USPAT	OR	ON	2005/07/16 21:58
S12 6	1	"5157654".PN. and cross\$1point with routing	USPAT	OR	ON	2005/07/16 22:16
S12 7	0	"5157654".PN. and input adj module and "Fig.10"	USPAT	OR .	ON	2005/07/16 22:04
S12 8	1	"5157654".PN. and input adj module	USPAT	OR	ON	2005/07/16 22:04
S12 9	1	"5157654".PN. and cross\$1point with routing and determ\$3	USPAT	OR	ON	2005/07/16 22:17
S13 0	0	"5157654".PN. and cross\$1point with routing same determ\$3	USPAT	OR	ON	2005/07/16 22:17